AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings of claims in the application.

LISTING OF CLAIMS:

What is claimed is:

1   1.   (Currently Amended) A method for ~~controllng~~ controlling a computer
2   system having a communication link processor and employing
3   recording mechanisms with a FIFO ~~buffer~~ for buffered event
4   entries, comprising the steps of:
5   ~~Controling~~ controlling descreet events for an
6   asynchronous event ~~storing and~~ recording mechanism by
7   storing discreet events into the FIFO at a location
8   determined by ~~a~~ an event write pointer; and
9   causing an attached processor to read the recording
10   mechanism's FIFO at a location determined by ~~a~~ an event
11   read pointer;
12   said recording mechanism ~~conditionally~~ returning event
   and status information; and

1   ~~conditionally~~ incrementing the FIFO read pointer, and
2   causing said recording mechanism to store one or more
3   of multiple event entries into main memory of said
     processor , wherein the said multiple storing of entries
1   from the FIFO does not affect the state of the FIFO event
2   read pointer or event write pointer.
3   .
4
5   2.   (Currently Amended) The method as recited in claim 1,
     wherein ~~the~~ a fullness indication of the FIFO is returned
1   in ~~the~~ read information to said processor as the value of
2   the FIFO event read pointer and event write pointer.
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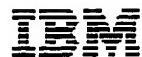
3.   (Original) The method as recited in claim 1, wherein
1   the recording mechanism returns:
2   system status when the FIFO is completely empty; and
3   an event description when the FIFO has one or more
valid entries.

4.   (Currently Amended) The method as recited in claim 1,
wherein the processor can instruct the recording mechanism
to store multiple entries into the processor's main memory _
from any of said recording mechanisms.

5.   (Currently Amended) The method as recited in claim 4,
wherein the said multiple storing of entries from the FIFO
does not affect the state of the FIFO read pointer or write
pointer in any of the recording mechanisms employed.